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O ECE-C474/475 ASIC Design I/II O ECE-E421/422/434 Advanced Electronic Circuits O ECE-C355 Computer Organization And Architecture O ECE-E352 Analog Electronics Junior Design Engineer, Micron Semiconductor Technology Co., Ltd, Shanghai, China. March 2007-March 2008 Full Custom DRAM Integrat 1th, 2024

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• ASME IGTI Committee Best Paper Award, 2017: ASME Turbo Expo 2017:

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Packet Delay In MANET", In Proceedings Of International Conference On Information Technology, 2009 (ICIT 2009), Bhubaneswar, December 2009, Pages 50-54. 22. Alekha Kumar Mishra And Ashok Kumar Turuk, "A Survey Of Security Threats In Various Layers Of Wireless Sensor Networks", In Proceedings 3th, 2024

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