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(D. P. Bovet And M. Cesati, “Understanding The Linux Kernel”, 3rd Edition) 5 . Real-time Systems Lab, Computer Science And Engineering, ASU . Interrupt Handling Depends On The Type Of Interrupts I/O Interrupts Timer Interrupts Interprocessor Interrupts Unlike Exceptions, Interrupts Are “out Of Context” Events Generally Associated With A Specific Device That Delivers A Signal On A ... 1th, 2024

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14 Page Fault 18 Machine Check 32-255 User Defined Interrupts - Every Exception/Interrupt Type Is Assigned A Number: -its
Vector - When An Interrupt Occurs, The Vector Determines What Code Is Invoked To Handle The Interrupt. - JOS Example:
Vector 14 → Page Fault Handler Vector 32 → Clock Handler → Scheduler 3th, 2024

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Some Understanding Of Embe Dded Programming, Such As Programming In C, And So On. It Also Assumes The Use Of The
ARM Developer Suite (ADS) As It Refers To Code Examples Found In The ADS Directories. In Addition, Setup And Use Of The
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Very Similar To That From The 32-bit System Shown Earlier. CPU0 CPU1 27: 1705 34141 IO-SAPIC-level Qla1280 40: 0 0
SAPIC Perfmon 43: 913 6960 IO-SAPIC-level Eth0 47: 267 2th, 2024

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Plete, However, The Processor Would Resume Exactly Where It Left Off. This Method Is CalledInterrupt. It Is Easy To See That

System Throughput Would Drastically Increase, And Thus More Tasks Could Be Assumed By The Micro-computer To Further Enhance Its Cost Effectiveness. The Programm 2th, 2024

Lab08 - Interrupt Handling And Stepper Motor Controller

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