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Allmän ...Condition Mainly Used With Binary Logic
Elements Where The Logic State 1 (TRUE) Is Converted
To A Logic State 0 (FALSE) Or Vice Versa [IEC
60617-12, IEC 61082-2] 3.20 Logic Inversion Condition
Mainly Used With Binary Logic Elements Where A
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Intended As Guide. Choise Of Equipment May Change
The Mentioned Measurements Reach With Outriggers
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SENSORS ...75.5351.02 EN 20080317 (75.5350) Page
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Microwave And Microprocessor Transmitter Frequency:
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