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MP, CSE, VCET 3 » The Intel 8086/8088 » 16-bit Pipelined Microprocessors » Addressed 1M Bytes (1M Byte = 1024K Bytes = 1024 * 1024 Bytes = 1,048,576 Bytes) Of Memory » Executed 2.5 MIPs (millions Of Instructions Per Second) » Its Instruction Set Contained Over 20,000 Instructions » A Small 6- Or 4-byte Instruction Cache Or 2th, 2024

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Working Steps For C Interrupt Programming Include Interrupt File #include Sei () & Cli () Instructions For I Flag. Define ISR ISR (interrupt Vector Name) { // Our Program } For Interrupt Vector Name We Must Use ISR Name From T 3th, 2024

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C) What Is The Use Of Bit -Set -Reset Mode Of 8255? [2M] D) What Is The Difference Between A Long Jump (LJMP) And A Short Jump (SJMP) Instruction Of 8051? [3M] E) What Is The Purpose Of Pseudo -instructions? Explain Briefly. [3M] F) Which Ports Of PIC18 Are Bit Addressable? [2M] PART -B 2. A) What Are Microprocessors? 1th, 2024

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