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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ May 1th. 2024

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## Cluster-based Haldane States In Spin-1/2 Cluster Chains

States, And That Of Neighboring Rungs Is Antisymmetric Due To The Antiferromagnetic Intercluster Interaction. Consequently, This Ground State Corresponds To The AKLT State, Where The Real S=1 Spin Is Regarded As An Effective S=1 Spin Of A Rung. These Examples Intuitively Give Us The Fo Mar 1th, 2024

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