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FPGA Advantages: $\frac{3}{4}$ noise Is Easy To Control After Initial Quantization $\frac{3}{4}$ highly Linear (with Limited Dynamic Range)
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Discrete Time Signal Processing Computer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-time And Digital Signal Respec-tively. As We Know From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal 3th, 2024.
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