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2E FPGA And The 18V00 ROM On The D2-SB, And Any Programmable Devices On
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The JTAG Sca May 12th, 2024 Digilent Pegasus Board Reference Manual ... Pegasus

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Single Tone Signal, An Eclipse Z7 Zynq-7000 APSoC (All Programmable System-on-Chip) Board Together With The Zmod AWG 1411 Digital-to-analog Con May 19th, 2024.

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