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Electronics And Communication Engineering Department 2nd Year 2nd Semester: A. Theory Sl. No. Course Code Course Name Contact Hours/Week Credit L T P Total Points

1	CHEM2001	Basic Environmental Engineering And Ecology	3	0	0	3	3	2	ECEN2201	EM Theory & Transmission Line	3	0	0	3	3	3
	ECEN2002	Digital Electronics	3	0	0	3	3	4	ECEN2203	Analog Communication	3	0	0	3	3	5
	ECEN2204	Solid State Devices	3	0	0	3	3	3								

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Communication Engineering Department Lab Names Space & No. Students Can Perform Experiment Quality Of Instruments Basic Electronics Lab 1250 Sqm, 40 All The Basic Equipment Including CRO, Function Generator, Multi Utility Power Supply Unit, Trainer Kit, Modern Digital Multimeter, Digital Electronics Lab 1250 Sqm, 40 All The Basic Equipment Including CRO, Function Generator, Feb 22th, 2024.

Department Of Electronics And Communication Engineering

4. G.F.Sudha And B.Nanchariah, "A Distributed Routing Algorithm For Data Gram Traffic In Teledesic System", International Conference On Wireless Communication Networks, SSN College Of Engineering, Chennai, 27-28 June 2003.
5. G.F.Sudha And T.G.Palanivelu, "Visibility Enhancement Of Subsurface Tissue Images", Mar 11th, 2024

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ...Using Lab View 8.6, International Journal Of Electronics, Communication & Instrumentation Engineering Research And Development (IJECIERD) Jun 2013, Vol. 3, Issue 2, Pp:195-200.

3. K.SudhaRani, T. C. Sarma & K.Satya Prasad, Text File Encryption Using FFT Technique In Lab View 8.6, Intern Mar 20th, 2024

Department Of Electronics And Communication M.Tech In ...1 Introduction To Embedded Systems Shibu K V Tata McGraw Hill Education Private Limited, 2009 . SRI SIDDHARTHA INSTITUTE OF TECHNOLOGY- TUMAKURU (A Constituent College Of Siddhartha Aca Jan 11th, 2024.

Department Of Electronics And Communication Engineering ...EMBEDDED SYSTEMS
LAB MANUAL (On ARM Microcontroller And RTOS) I Year M.Tech I - Sem Prepared By
Ch. Madhuri Devi Asso.Prof Easari. Parushuramu Asst.Proff Under Prof. K. Ashok
Babu HOD Of ECE Dept. SriIndu College Of Engineering And Technology (an
Autonomous Institution Under JNTUH) S Feb 16th, 2024DEPARTMENT OF
ELECTRONICS & COMMUNICATION ...Athanasios Papoulis And S. Unnikrishna Pillai,
“Probability, Random Variables And Stochastic Processes”, PHI, 4th Edition, 2002.
References: ... Axiom 2: $P(S) = 1$. Which Means That The Probability Of A Sample
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2024.

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Logic Gates And (ii) NAND Gates. (b) Full Subtractor Using (i) Basic Logic Gates And
(ii) NANAD Gates. 3 Design And Implement 4-bit Parallel Adder/ Subtractor Using IC

7483. 4 Design And Implementation Of 5-bit Magnitude Comparator Using IC 7485.

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Department Of Electronics And Communication M.Tech In VLSI ... LITHOGRAPHY AND RELATIVE PLASMA ETCHING: Optical Lithography, Electron Lithography, X-Ray Lithography, Ion ... 1 Fundamentals Of Modern VLSI Devices Yuan Taur, Tak. H. Ning University Press, ... To Make The Students Learn The Principles, Operations And Applications Of Feb 29th, 2024 ELECTRONICS: SWANCC: Abt Electronics Park Ridge Recycling ... "WHITE GOODS" (APPLIANCES)—stoves, Refrigerators, Freezers, Air

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