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#### Section 1 8051 Microcontroller Instruction Set

Mnemonic Description Byte Oscillator Period LOGICAL OPERATIONS ANL A,Rn AND Register To Accumulator 112 ANL A,direct AND Direct Byte To Accumulator 212 ANL A,@Ri AND Indirect RAM To Accumulator 112 ANL A,#data AND Immediate Data To Accumulator 212 ANL Direct,A AND Accumulator To Direct Byte 2 Jan 21th, 2024

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