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Computing Paradigms", IEEE Transactions On Emerging Topics In Computing, 2016.

9. Yu Bai† And Mingjie Lin\*. "Stochastic-Based Spin-Programmable Gate Array With Emerging MTJ Device Technology". 3th, 2024C.1 Introduction - UCF Computer ScienceC.1 Introduction C-5 Data Loaded From Memory. In The Case Of A Store, The Second Register Operand Is The Source Of The Data That Is Stored Into Memory. The Instructions Load Word (LD) And Store Word (SD) Load Or Store The Entire 64-bit Register Contents. 3. Branches And Jumps—Branches Are Conditional Transfers Of Control. There 3th, 2024.

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