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Design And Implementation Of FPGA Based Vending Machine ...SEEL Singled Electron Encoded Logic. The Designed Circuit Is Tested And Its Power And Switching Time Is Compared With The CMOS Technology. III. IMPLIMENTATION [1][11][4] The Propose Machine State Diagram Is Constructed To Vend Out Seven Integrated Circuit (IC) That Is AND-GATE,OR-GATE,NOT-GATE,NOR-GATE,NAND-GATE,EXOR-GATE,EXNORGATE.Author: Edison Kho, Manoj Kumar 2th, 2024

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